Indian Journal of Electrical Engineering and Modern Technology

Volume 8, Number 2 (2018)

Contents

Bist Architecture For Digital System Using Ca-Lfsr And Msic Test Pattern Generators A.V.Mutyalamma ¹ , G.Rakesh Chowdary ² , T.V.Rama Krishna ³	91-102
Studies on NDT Curve establishment of AA 5052 Honey Comb Sandwich Panel using Fokker Bond Tester P. R. Jeyakrishnan ^{1,*} , A. Geetha ² ,	103-118
Modelling and Simulation of Vector Control of DFIG Using SEQUEL Sheena Latif and Dr. Lekshmi. A	119-137
Kalman Filtering Techniques For Fault Detection and Diagnosis In Continuously Stirred Tank Reactor (Cstr) P V Sunil Nag, Sabarish R P Nair, Gowtham M, Sibichakravarthy V, T D Shivendran, Manjunath S	139-148
Design and Implementation of Low Power Multistage AES S Box <i>Rajasekar P and Dr. H. Mangalam</i>	149-160
Analysis on the Effect of V2V-based AEB Systems According to Various Road Alignments and Surface Conditions Hyeonggeun Mun, Gyoungeun Kim, Byeongwoo Kim	161-172
A Critique on Authentic Materials for Effective ESP Materials Design A Rajesh and Dr.SarikaTyagi	173-179