International Journal of MATERIAL RESEARCH, ELECTRONICS AND ELECTRICAL SYSTEM

VOLUME 9

NUMBER 1

JUNE 2016

Contents

	TI 16 Time For success A solve in Board on	1
A Signal Processi	ng Tool for Time-Frequency Analysis Based on	1-9
Reassignment wit		
	Pei–Wei Shan and Ming Li	
	armonics Analysis Using Multi-layer Feed- Forward Artificial	11-24
Neural Network N		
•	Dharmendra Kumar Singh and A. S. Zadgaonkar	
Benefits of Hybrid	d Hosting Over Conventional Hosting	25-29
•	Azeem Haider and Ashwani Kumar	
Emotional Bacterial Foraging Optimization		31-36
•	Anuradha Limbraj Borkar, S. D. Deshmukh and V. M. Kulkarni	
VLSI Design Ana	llysis and Comparison of SRAM Cells using Cadence Tool	37-45
•	Shyam Akashe, Ankit Srivastava and Sanjay Sharma	
Analog to Digital	and Digital to Analog Converter using VLSI Technology	47-50
•	Rupali J. Jillawar	
Pilgrims Tracking	and Identification Using RFID Technology	51-60
*	Mohammed Abdul Aziz and B. Venkata Ramanaiah	
Sensors in Railwa	ay Track Accident Avoidance	61-65
•	Prajakta Kulkarni and Navin Srivastava	
Three Dimension	al Rotation Technique	67-70
•	Hamza, Shoaib Inamdar and Rohit Kandul	
An Insight to Ca	Il Blocking Probabilities of Channel Assignment Schemes	71-87
in magne to Ca	M.L.S.N.S. Lakshmi, M.S.L. Ratnavathi, B. Anusha Sumanthika and	
	S. Goni Krishna	