

INTERNATIONAL JOURNAL OF ADVANCE VLSI DESIGN

VOLUME 13

NUMBER 2

July-December, 2023

Contents



1. FPGA Implementation of OTPES Cryptographic Algorithm 79-90
for Channel Security
*B. Murali Krishna, Syed Shameem,
P. Srikanth Reddy, V. Sasikanth Reddy,
J.S.S. Akhil, D. Prasanna Kumar,
Ch. Dileep Kumar, K. Praveen Kumar and M. Sowmya*
2. Face Recognition using 2-DPCA, ICA, 2-DWT, Neural Network and SVM 91-106
K. Raju and Y. Srinivasa Rao
3. Multimodal Home Security System using IoT and Raspberry Pi 107-116
S. Neeraja and N. Venkatram
4. Frequency Estimation of Seismic Signal Using Eigen Vector Method 117-126
V. Gopi Tilak and S. Koteswara Rao
5. Performance Analysis of GWO, GA and PSO Optimized FOPID and 127-150
PSS for SMIB System
Atul M. Gajare and R.P. Singh
6. Research Challenges in Multimodal Biometrics – A Critical Review 151-162
*Ayesha Tarannum,
Srinivasulu Tadisetty and Md. Zia Ur Rahman*
7. Novel Segmentation Technique for Target Tracking in 163-170
Synthetic Aperture Radars
B. Malakonda Reddy and Md Zia Ur Rahman
8. Simplified Algorithms for Artifact Removal in Brian Computer 171-180
Interface Applications
M.V.V.S. Prasad and T. Ranga Babu

